

$$uv = \{u, v\}$$

whatever-first search

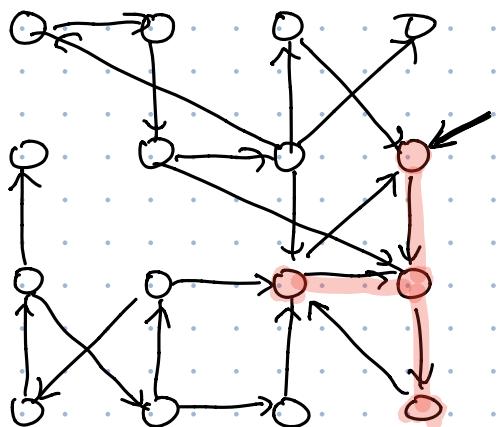
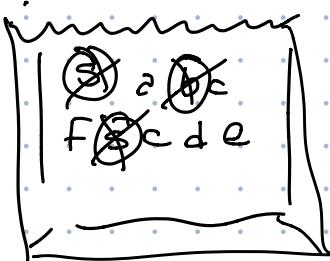
traversal \leftarrow connectivity
components
 \searrow reachability

$$\text{time} = O(V+E)$$

$$\text{connected} \Rightarrow E \geq V-1$$

$$V \leq E+1$$

$$\text{time} = O(E)$$



$$u \rightarrow v = (u, v)$$

keep a bag of vertices, init S

while bag not empty
remove v from bag
if v unmarked
mark v
for all edges $v \rightarrow w$
put w in bag



u can reach v

DFS(v):

mark v
PREVISIT(v)
for every edge $v \rightarrow w$
if w is unmarked
parent(w) $\leftarrow v$
DFS(w)
POSTVISIT(v)

DFS ALL(G):

PREPROCESS(G)
for all vertices v
unmark v
for all vertices v
if v is unmarked
DFS(v)

$O(V+E)$ time

not counting PREP + VISIT

DFS():

```

mark v
v.pre  $\leftarrow$  clock++
for every edge v  $\rightarrow$  w
  if w is unmarked
    parent(w)  $\leftarrow$  v
    DFS(w)
v.post  $\leftarrow$  clock++

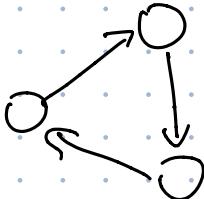
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DFSALL(G):

```

clock  $\leftarrow$  0
for all vertices v
  unmark v
for all vertices v
  if v is unmarked
    DFS(v)

```



Lemma: After DFSALL(G)

If G has a dir. cycle, then
for some edge v \rightarrow w
we have $v.\text{post} < w.\text{post}$



IF DFS reaches v first:

$$v.\text{pre} < w.\text{pre} < w.\text{post} < v.\text{post}$$

IF DFS reaches w first:

$$w.\text{pre} < v.\text{pre}$$

Suppose there is a dir. cycle

Let w be first vertex reached
by DFS in that cycle
Let v \rightarrow w be edge in cycle



① w can reach v.

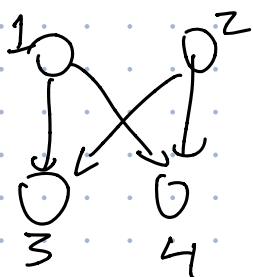
$$w.\text{pre} < v.\text{pre} < v.\text{post} < w.\text{post}$$

② w can't reach v.



$$w.\text{pre} < w.\text{post} < v.\text{pre} < v.\text{post}$$

Iff $v.\text{post} > w.\text{post}$ for all $v \rightarrow w$ then [G has no directed cycles]



G is a dag.

Every dag has 2 topological ordering

$$\text{num}(v) < \text{num}(w) \text{ for all } v \rightarrow w$$

Proof: Let $\text{num}(v) = V - \text{post}(v)$ \square

Topological sort = reverse postorder

Preprocess
clock $\leftarrow \sqrt{\# \text{vertices}}$

Previsit(v):
 y_{aux}

$O(V+E)$ time

Postvisit(v):

$\text{Top}[\text{clock--}] \leftarrow v$

ALL DP = DFS