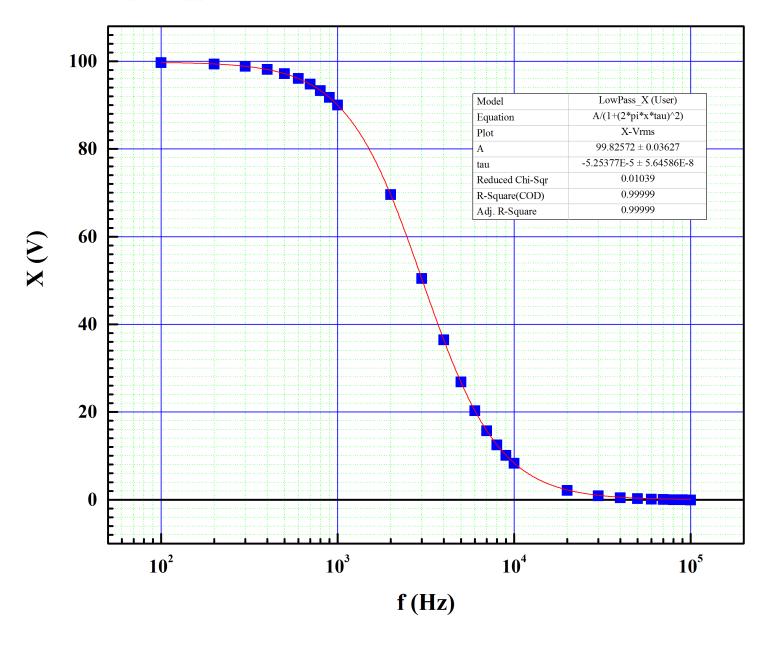
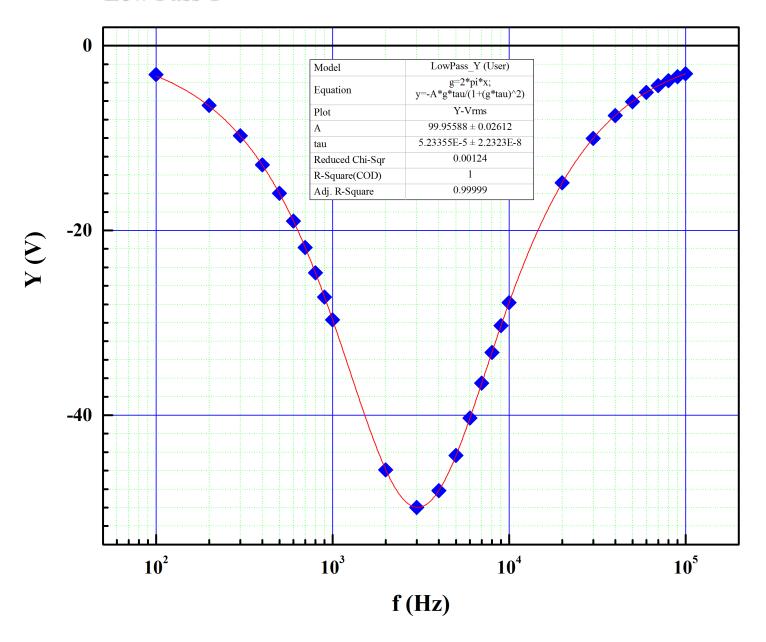
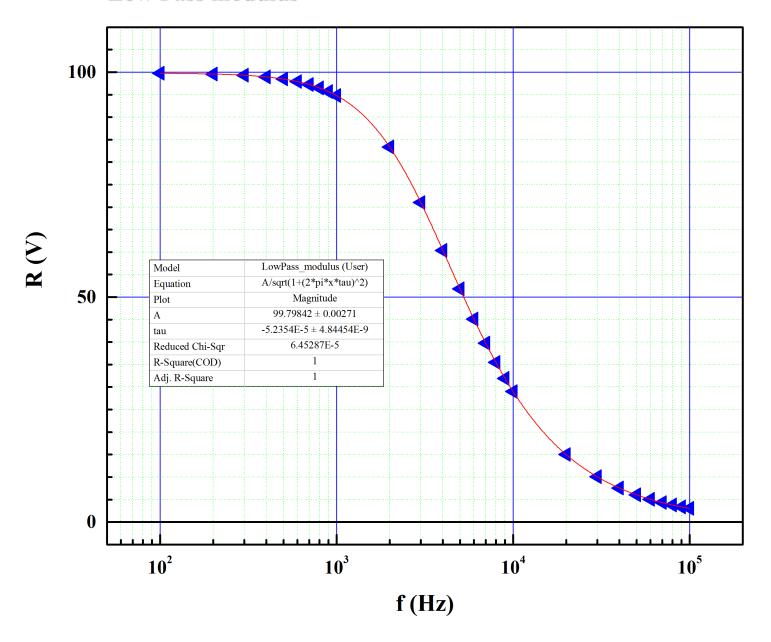
Low Pass X



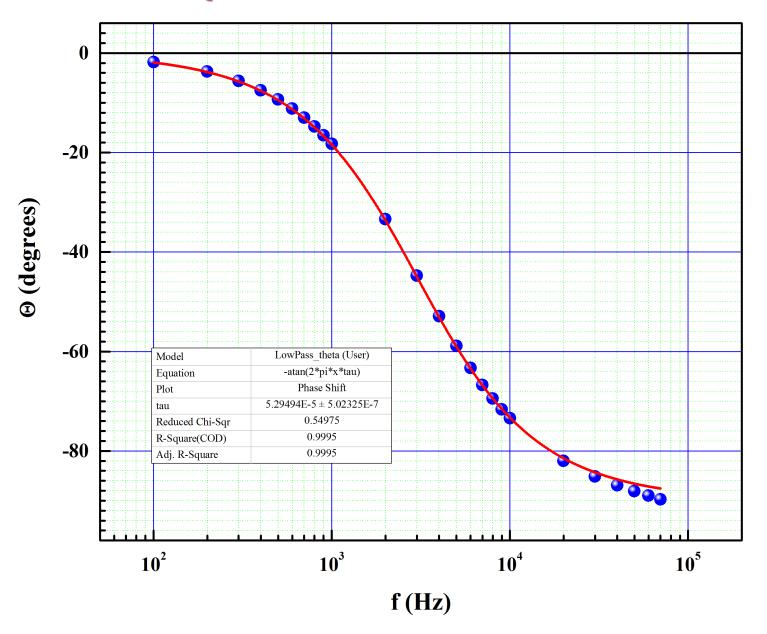
Low Pass Y



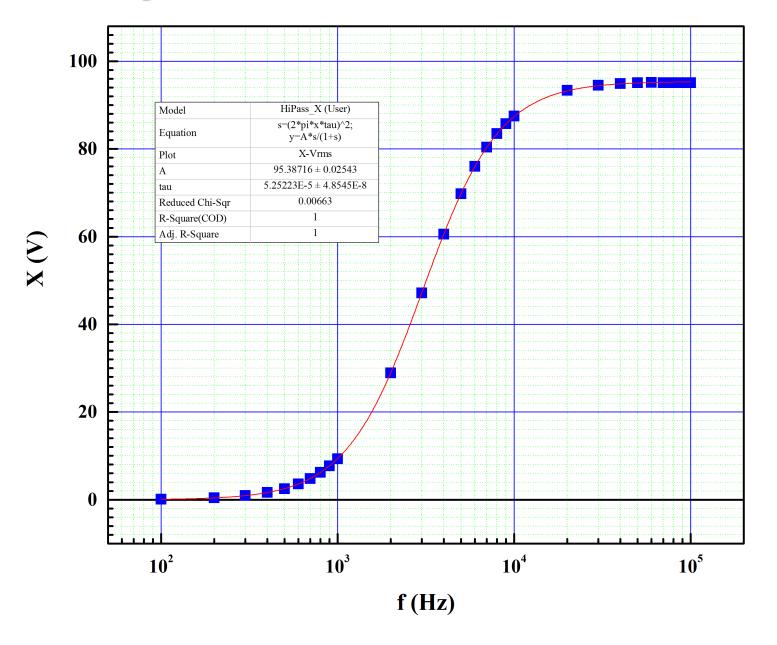
Low Pass modulus



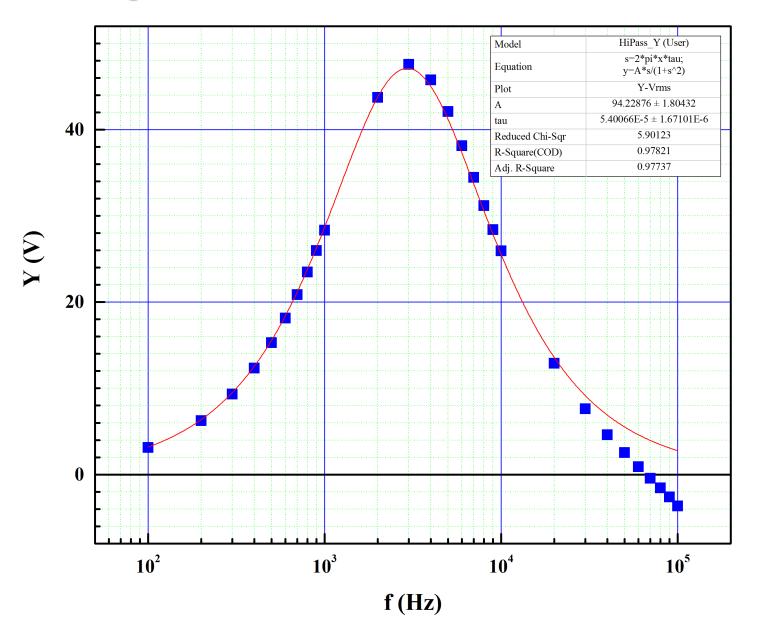
Low Pass phase



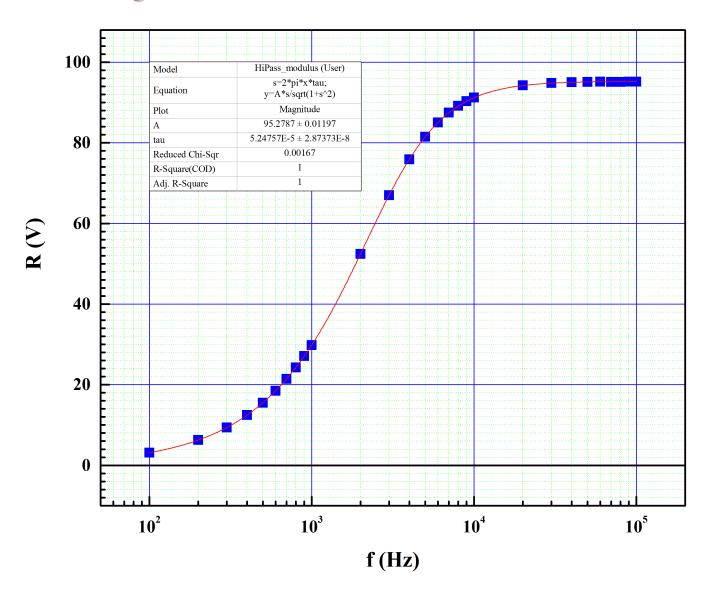
High Pass X



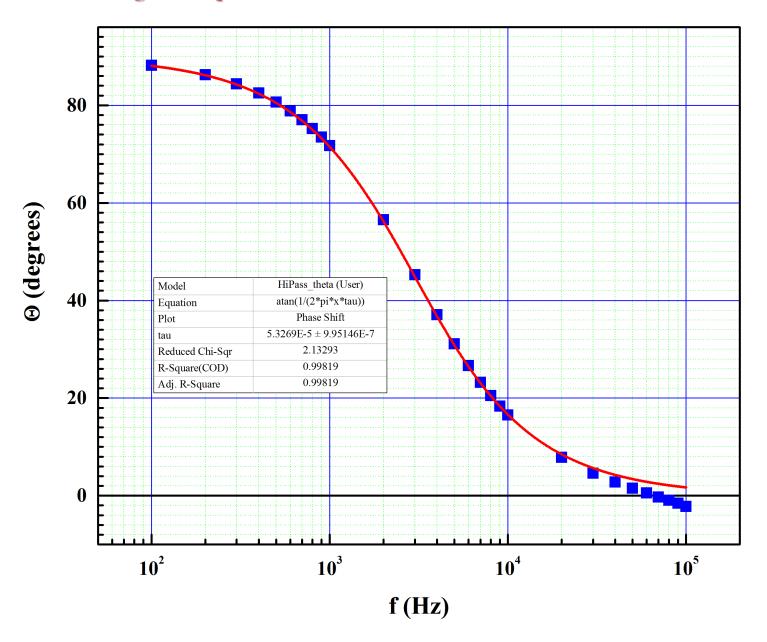
High Pass Y



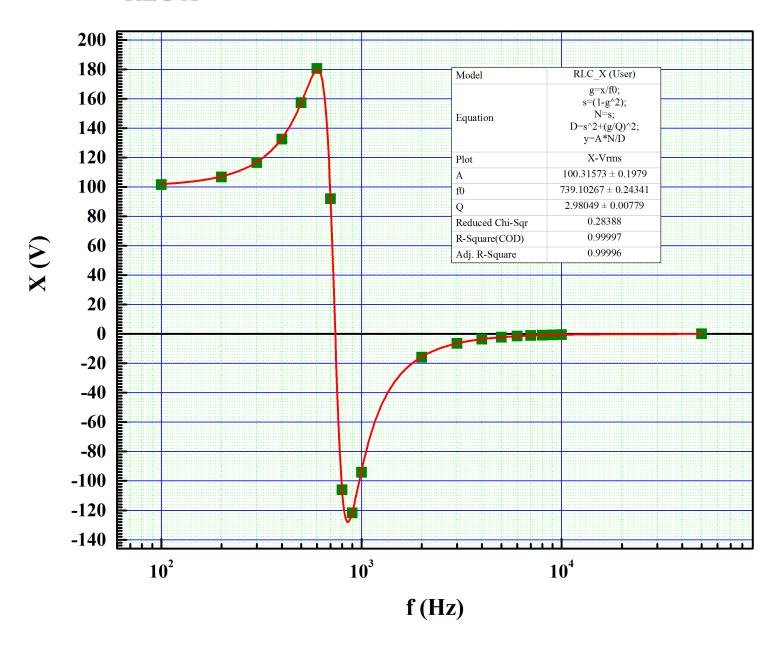
High Pass modulus



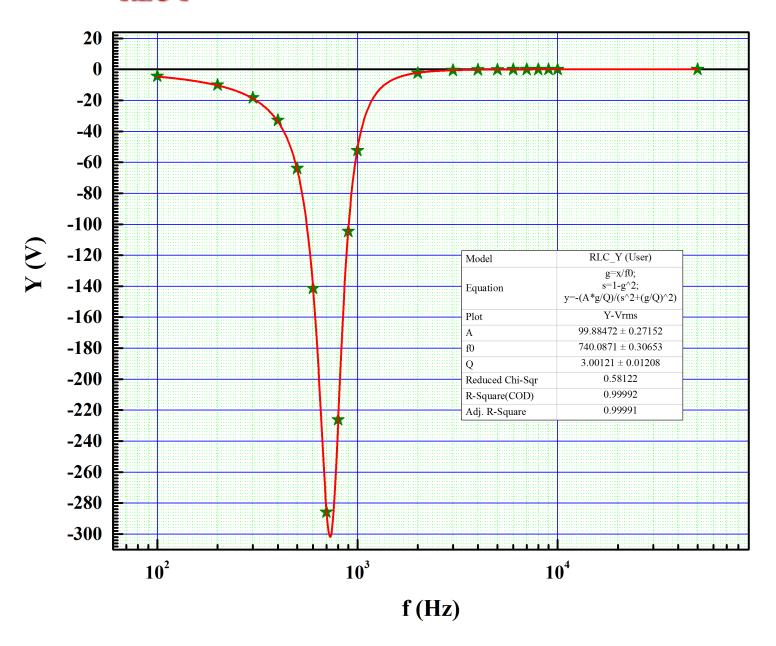
High Pass phase



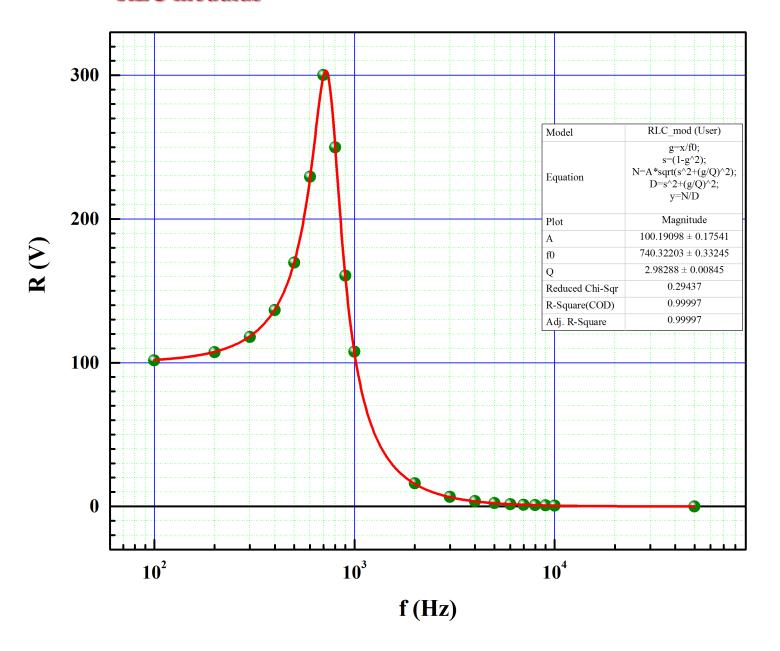
RLC X



RLC Y



RLC modulus



RLC phase

